

REMARKS

Summary Of The Office Action

Claims 1-3 are pending in the application. Each of these claims is rejected under 35 U.S.C. § 103 as being unpatentable over the admitted prior art in view of Hiroshi (JP-6244754). Our comments with respect to this rejection are set forth below.

Analysis Of The Rejection Of Claims 1-3

The rejection of claims 1 and 3 is similar to that contained in the previous Office Action, except that the Examiner now cites a different part of Hiroshi as teaching the claimed detector and the claimed AGC. That is, in the present Office Action, the Examiner continues to state that Applicant's admitted prior art Fig. 1 shows all of the claimed elements except for the detector and the AGC, and the Examiner continues to rely on Hiroshi as teaching these missing elements. However, the Examiner now refers to Figs. 27(a) and (b) as teaching the claimed detector and AGC. Additionally, the Examiner has withdrawn his indication that claim 2 is directed to allowable subject matter and relies on Figs. 27 (a) and (b) for teaching the elements of dependent claim 2.

Applicant respectfully submits that claims 1-3 are not rendered obvious by the combination proposed by the Examiner at least because the combination of references fail to teach or suggest the claimed detector in combination with the claimed AGC.

In more detail, claim 1 recites "a detector for detecting a gain control level corresponding to the difference obtained by comparing the levels of the baseband signals of the two channels

RESPONSE UNDER 37 C.F.R. § 1.111
U.S. APP. No. 09/738,900

output by the filter with a predetermined level”. As noted above, the Examiner cites Figs. 27(a) and (b) of Hiroshi for this teaching. Applicant submits, however, that Fig. 27(b) shows a gain control circuit 4202 that outputs a signal based upon an integrated value of a difference between a reference signal and the sum of two other signals. This integrated value is applied to the amplifiers and is not analogous to the claimed “gain control level corresponding to the difference obtained by comparing the levels of the baseband signals of the two channels output by the filter with a predetermined level” and the AGC controlling gains according to the gain control level detected by the detector. That is, because the output of element 4202 is an integrated value it does not correspond to the output of the subtractor shown in Fig. 27(b). At least for this reason, Hiroshi does not teach or suggest the claimed detector or the claimed AGC for controlling gains according to the gain control level detected by the detector.

With respect to claim 2, Applicant submits that Hiroshi does not teach or suggest the claimed “level-comparing unit for generating a gain control level corresponding to the difference obtained by comparing the levels of the signals detected by the adding unit with a predetermined level.” That is, as explained above in the discussion of claim 1, element 4202 of Hiroshi does not generate a gain control level corresponding to the difference obtained by comparing the levels of the signals detected by the adding unit with a predetermined level, but instead generates a value which is the integrated value of the signal output by the subtractor.

With regard to claim 3, Applicant submits that this claim is patentable over the applied references for reasons similar to those discussed above with respect to claim 1.

RESPONSE UNDER 37 C.F.R. § 1.111
U.S. APP. No. 09/738,900

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

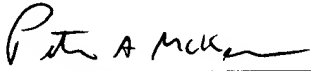
Respectfully submitted,

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